

10/679/24 ✓

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				Application Number	<del>Not yet assigned</del>
				Filing Date	Herewith 10/2/2003 ✓
				First Named Inventor:	Suman Datta et al.
				Art Unit	<del>Not yet assigned</del> 2827 ✓
				Examiner Name	<del>Not yet assigned</del> Huan Hoang ✓
				Attorney Docket Number	42P16682
She t	1	of	2		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
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FOREIGN PATENT DOCUMENTS							
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Examiner Signature	Huan Hoang	Date Considered	5/15/05
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				Application Number	<del>Not yet assigned</del> 10/679124 ✓
				Filing Date	<del>10/02/2003</del> 10/02/2003 ✓
				First Named Inventor:	Suman Datta et al.
				Art Unit	<del>Not yet assigned</del> 2827 ✓
				Examiner Name	<del>Not yet assigned</del> Huan Hoang
				Attorney Docket Number	42P16682
Sheet	2	of	2		
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
HH		Evert Seevinck, "Static-Noise Margin Analysis of MOS SRAM Cells", IEEE Journal of Solid State Circuits, Vol. Sc-22, No. 5, October 1987, pp 748-754			
HH		Peter A. Stolk et al., "Modeling Statistical Dopant Fluctuations in MOS Transistors", IEEE Transactions on Electron Devices, Vol. 45, No. 9, September 1998, pp. 1960-1971			
checked		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/227,068, Filed August 23, 2002			
checked		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/367,263, Filed February 14, 2003			

Examiner Signature	Huan Hoang	Date Considered	5/15/05
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